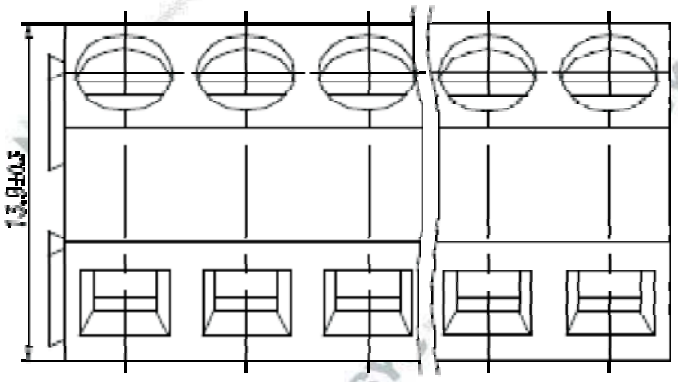
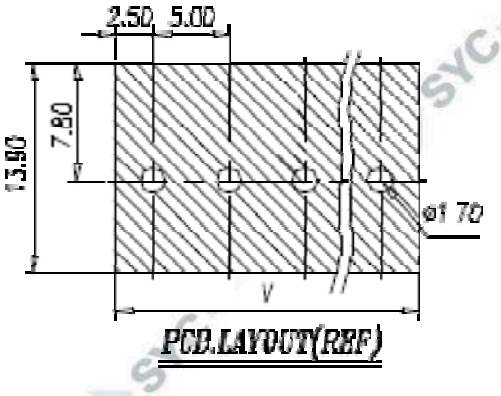
SYC SEMICONDUCTORES Y COMPONENTES

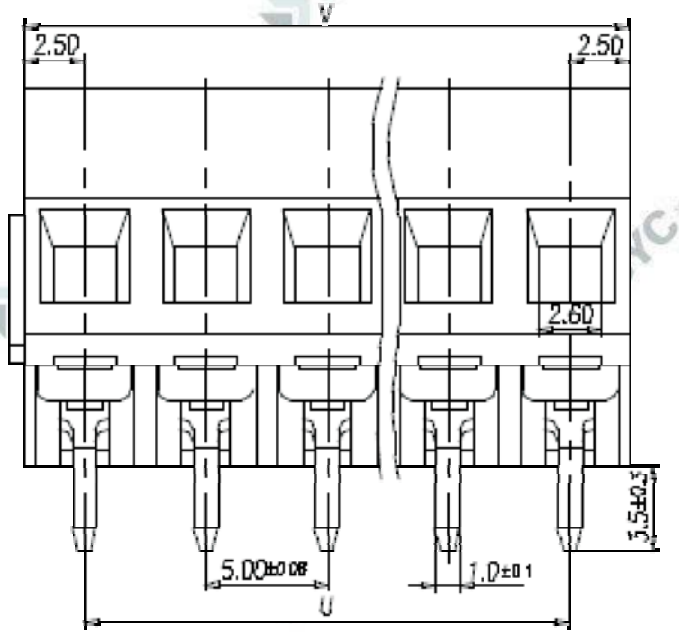
PARANA 274
(C1017AAF) BUENOS AIRES
TEL/FAX: 4371-5300 (L.ROT)
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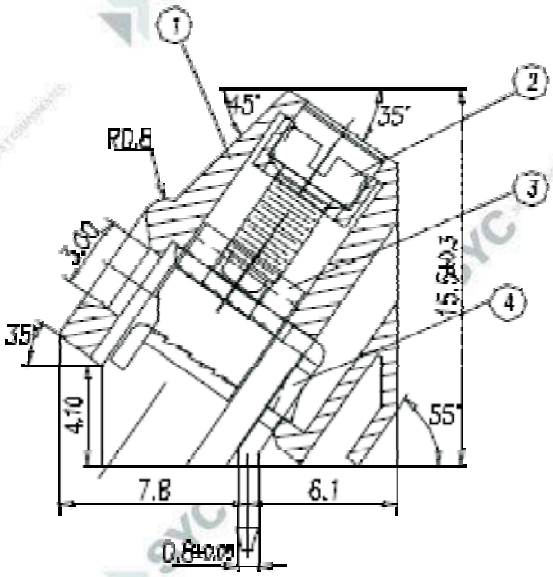
CODIGO	DESCRIPCION
EK500A-XXP	Bornera de X polos paso 5mm a 45º





PCB LAYOUT(REF)





P	2	3	4	5	6	7	8	9	10	11	12	13
U	5.00	10.00	15.00	20.00	25.00	30.00	35.00	40.00	45.00	50.00	55.00	60.00
V	10.00	15.00	20.00	25.00	30.00	35.00	40.00	45.00	50.00	55.00	60.00	65.00

P	14	15	16	17	18	19	20	21	22	23	24
U	65.00	70.00	75.00	80.00	85.00	90.00	95.00	100.00	105.00	110.00	115.00
V	70.00	75.00	80.00	85.00	90.00	95.00	100.00	105.00	110.00	115.00	120.00

Tolerances for PITCH ERROR

NOTES:

1. Rating. 300V 20A
   PITCH: 5.00mm
2. Insulation Withstands Voltage:
   AC 1500V/MIN
3. Insulation Resistance.
   500MΩ or more at DC500V
4. Using Temperature Range:
   −40℃~+105℃
5. Screw Torque Value: 4 kgf−cm
6. Suitable Electric Wire: ≧~12 AWG 24
7. Undimensioned Tolerances:

—	⌒	▱	0.05/mm
⊥	//	≒	0.2/mm
PITCH ARRAY			0.05/mm
UNDIMENSIONS TOLERANCES			±0.3